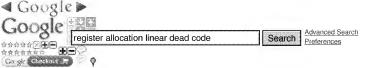
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Fig. Show options...

1. IPDFI Register Allocation Previously • Graph Coloring Allocators ...

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Linear order of code ignores dynamic control flow. • A temporary may reside in different locations ... After register allocation: 1. dead-code elimination ... www.cs.utexas.edu/users/mckinley/380C/lecs/14.pdf - Similar

# 2. <u>libjit-linear-scan-register-allocator - Project Hosting on Google Code</u>

**Dead-code** elimination (level 4 of optimization). Static live variable analysis (level 3, 4 of optimization). **Linear** scan **register allocation** algorithm ... **code**.google.com/p/libjit-linear-scan-register-allocator/ - <u>Cached</u> - <u>Similar</u>

# 3. Linear IR - Mono

This pass performs **dead code** elimination on single basic blocks. ... registers available for global **register allocation**. It uses a **linear** scan algorithm. ... www.mono-project.com/**Linear** IL - Cached - Similar

# 4. [PDF] Quality and Speed in Linear-scan Register Allocation

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to register allocation, called linear scan, and we compare it ..... each case, register allocation is preceded by dead code elim- ...

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opposed to **linear** scan **register allocation** live ranges along ... The algorithm produces **code** that is better than **linear** scan **register allocation** in terms of speed of .... searched for intervals that are **dead** i.e. whose end points ...

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#### 6. Linear Scan Register Allocation in a High-Performance Erlang Compiler

We have experimented with **register allocation**, focusing on **linear** scan, and reported on its performance in the context of a just-in-time native **code** ... www.springerlink.com/index/6MTX4LXEJGD1JUYK.pdf - Similar

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the temporary is  $\mathbf{dead}$  at all instructions outside the live interval; ..... the  $\mathbf{code}$  so much that the SCC-based  $\mathbf{linear}$  scan  $\mathbf{register}$  allocation becomes ...

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run copy propagation after **register allocation**. Finally, a **dead-code** elimination pass is added to remove unnecessary instructions and basic blocks from the ... reference.kfupm.edu.sa/.../quality\_and\_speed\_in\_linear\_scan\_registe\_107391.pdf - Similar

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# 10. Experimental evaluation and improvements to linear scan register ...

**LINEAR** SCAN **REGISTER ALLOCATION**. 1013 with a compiler for, e.g., C or FORTRAN **code** are the relatively high number of function calls, the ... doi.wiley.com/10.1002/spe.533 - Similar

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